

## Overview

This PD example is a simple demonstration based on the MCUXpresso SDK PD stack. The application use the board keys and debug console to test the PD functions. The demo works as DRP. When connect, the board can be source or sink.

## System Requirement

### Hardware requirements

- Two Type-C shield boards
- Two 9V DC power suppliers
- Type-C Cable
- Two hardwares (Tower module/base board, and so on) for a specific device, for example: two lpcpresso54114 boards
- Personal Computer

### Software requirements

- The project files are in:  
<MCUXpresso\_SDK\_Install>/boards/<board>/usb\_examples/usb\_pd/<rtos>/<toolchain>.

Note

The <rtos> is Bare Metal or FreeRTOS OS.

- Terminal tool.

## Getting Started

### Hardware Settings

- The EVK-MIMXRT685 board jumper settings:  
JP12 2-3.
- This board doesn't support "Power request SW" and "Power change SW".

For detailed instructions, see the appropriate board User's Guide.

Note

Set the hardware jumpers (Tower system/base module) to default settings.

### Prepare the example

1. For MCUXpresso, please reference to the MCUXpresso SDK USB Type-C PD Stack User's Guide to make sure the SDK\_DEBUGCONSOLE = 1 in project settings.
2. Download the program to the target board.
3. Power on Type-C shield board then power on development board.

## Run the example

1. Download this program to two boards, connect these two boards with Type-C cable.
2. Connect two boards' Debug USB port to the PC and open terminal.
3. If there are more than one port, please input '0-4' number in the debug console to select the tested port.
4. Test commands
  - Input '0' in the debug console, the follow menu will print in the debug console (the menu is little different for source and sink):



```
The menu is as follow for sink:
0. print menu
a. get partner source capabilities
b. request 5V
c. request high voltage
d. hard reset
e. soft reset
f. power role swap
g. data role swap
h. vconn swap
i. get partner sink capabilities
j. standard structured VDM test (only DFP can send enter mode)
l. send attention
m. test vendor structured VDM
n. test unstructured VDM
u. cable reset (not supported yet)
```

Figure 1: demo menu

- Input the menu to test the corresponding command.
  - For example: you input 'g', "data role swap" command will start, the debug console will print the result.
5. Some boards' default configuration is PD2.0. To test the PD3.0 command, PD\_CONFIG\_REVISION need be changed from PD\_SPEC\_REVISION\_20 to PD\_SPEC\_REVISION\_30 in usb\_pd\_config.h file. Then input '0' in the debug console, some PD3.0 related commands menu are added.
  6. For compliance test, PD\_CONFIG\_COMPLIANCE\_TEST\_ENABLE, PD\_CONFIG\_TRY\_SNK\_SUPPORT and PD\_CONFIG\_TRY\_SRC\_SUPPORT need be enable in usb\_pd\_config.h. There are five compliance test configurations in this demo: PD\_COMPLIANCE\_TEST\_DRP, PD\_COMPLIANCE\_TEST\_DRP\_TRY\_SNK, PD\_COMPLIANCE\_TEST\_DRP\_TRY\_SRC, PD\_COMPLIANCE\_TEST\_CONSUMER\_PROVIDER and PD\_COMPLIANCE\_TEST\_PROVIDER\_CONSUMER. There are five VIF files in the VIF directory corresponding to these five configurations.
  7. This demo support the auto policy feature test. There is one MACRO (PD\_TEST\_ENABLE\_AUTO\_POLICY) in pd\_board\_config.h file. It can be configured with five values: 0 – disable auto policy 1 – enable auto policy, the behaviors are: don't auto request, auto accept all request for pr swap, data swap and vconn swap 2 - enable auto policy, the behaviors are: auto request pr swap when works as sink, auto accept for swap to source/DFP/vconn source, auto reject for swap to sink/UFP/not vconn source, auto request power when working as sink; 3 - enable auto policy, the behaviors are: auto request data swap to DFP, auto accept for swap to sink/UFP/not vconn source, auto reject for swap to source/DFP/vconn source 4 - enable auto policy, the behaviors are: auto request vconn swap to vconn source, auto reject all request for pr swap, data swap and vconn swap.

### Note

After enable PD3.0 some target of USB PD demo can't build pass for a few boards because of board flash or ram code size limitation.