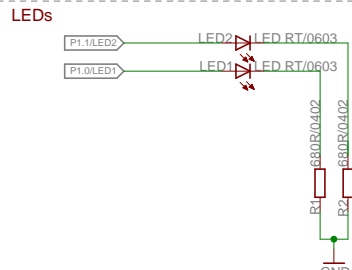
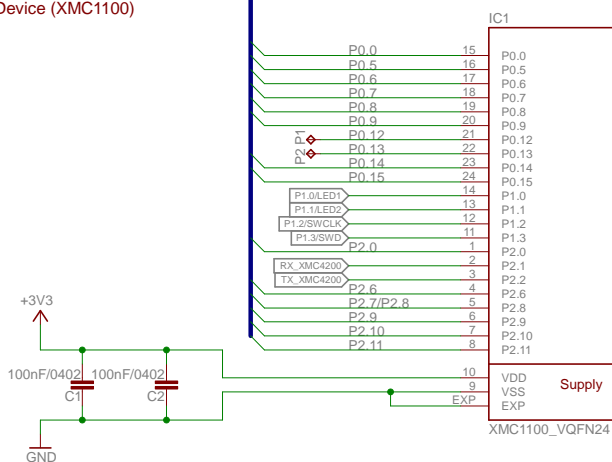
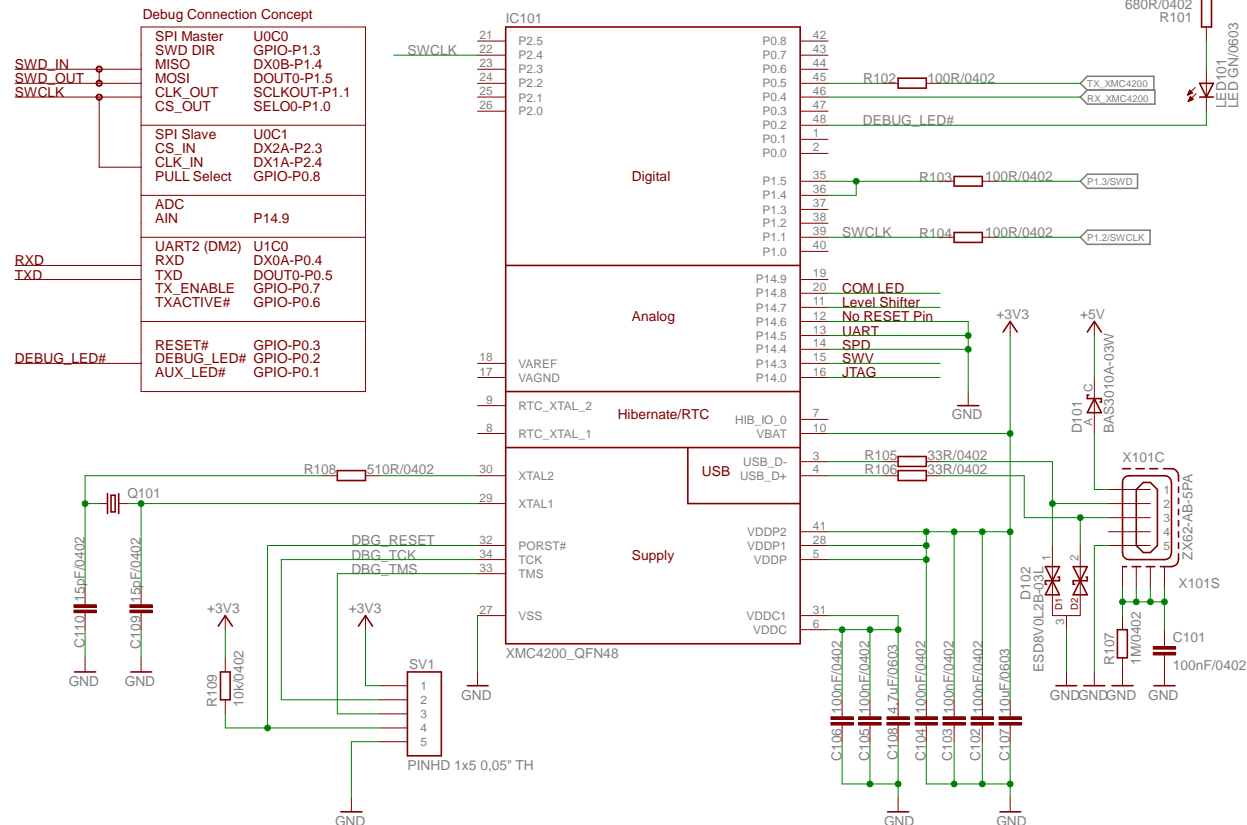


## E

### Target Device (XMC1100)



Debug Connection Concept		
	SPI Master	U0C0
	SWD DIR	GPIO-P1.3
	MISO	GPIO-P1.4
	MOSI	DOUT0-P1.5
	CLK_OUT	SECLKOUT-P1.1
CS_OUT	SELO0-P1.0	
	SPI Slave	U0C1
	CS_IN	DX2A-P2.3
	CLK_IN	DX1A-P2.4
	PULL Select	GPIO-P0.8
	ADC	P14.9
	AIN	
	UART2 (DM2)	U1C0
	RXD	DX0A-P0.4
	TXD	DX0D-P0.5
	TX_ENABLE	GPIO-P0.7
	TXACTIVE#	GPIO-P0.6
	RESET#	GPIO-P0.3
	DEBUG_LED#	GPIO-P0.2
	AUX_LED#	GPIO-P0.1



For further information and software examples please visit [www.infineon.com/xmc-dev](http://www.infineon.com/xmc-dev)

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.



Sheet: 1/1