

TLE9893_2QKW62S_UART_TRANSCEIVER

About this document

Scope and purpose

The aim of this guide is to present the scope, the implementation, the algorithm and a demonstration of the **TLE9893_2QKW62S_UART_TRANSCEIVER** example code for the TLE989x Infineon Embedded Power ICs based on Arm® Cortex® M3. This example code can be found in the Keil µVision Pack Installer.

The full functionalities and characteristics of the embedded power devices are described in the datasheets and user's manual. Please refer to these documents for more detailed information. Furthermore, a low level (line-by-line) description of the code is not the aim of this document, although occasionally some codeblocks might be reported if necessary to the comprehension.

Note: The following information is given as a hint for the implementation of the system only and shall not be regarded as a description or warranty of a certain functionality, condition or quality of the referred devices or presented software example.

Intended audience

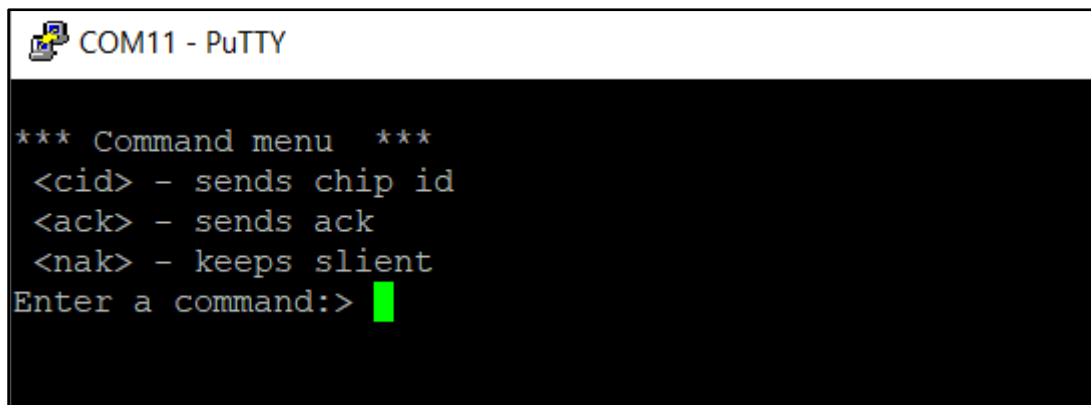
Design engineers, system engineers, embedded power designers

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1 Introduction

A command menu is written to the standard output (see Figure 1). A user-defined stdout target as well as a user-defined stdin target is implemented in the UART module. The redirection is established via the compiler-I/O-STDOUT RTE and compiler-I/O-STDIN RTE settings.



```

*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:>

```

Figure 1 Command menu

The pin P1.1 used as output is one debug pin on the USB device. The pin P1.2 used as input is another debug pin on the USB device.

Figure 2 shows the transmission of the ack command including the echo which can be seen on the UART transmit and receive buffer registers. The ACK response can be seen on the transmit buffer register immediately after the ack command is received.

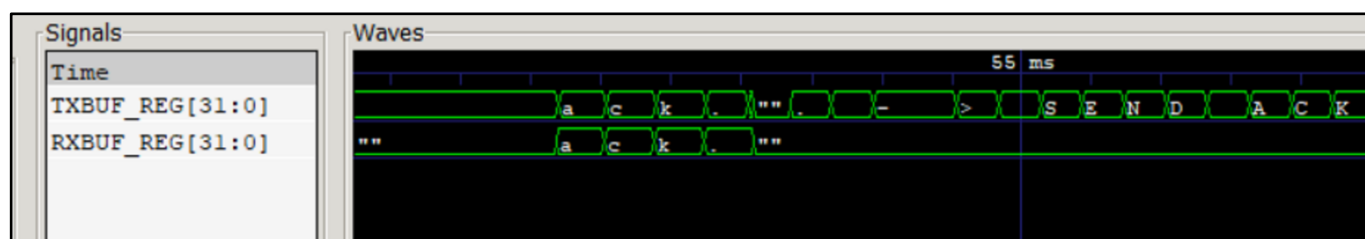
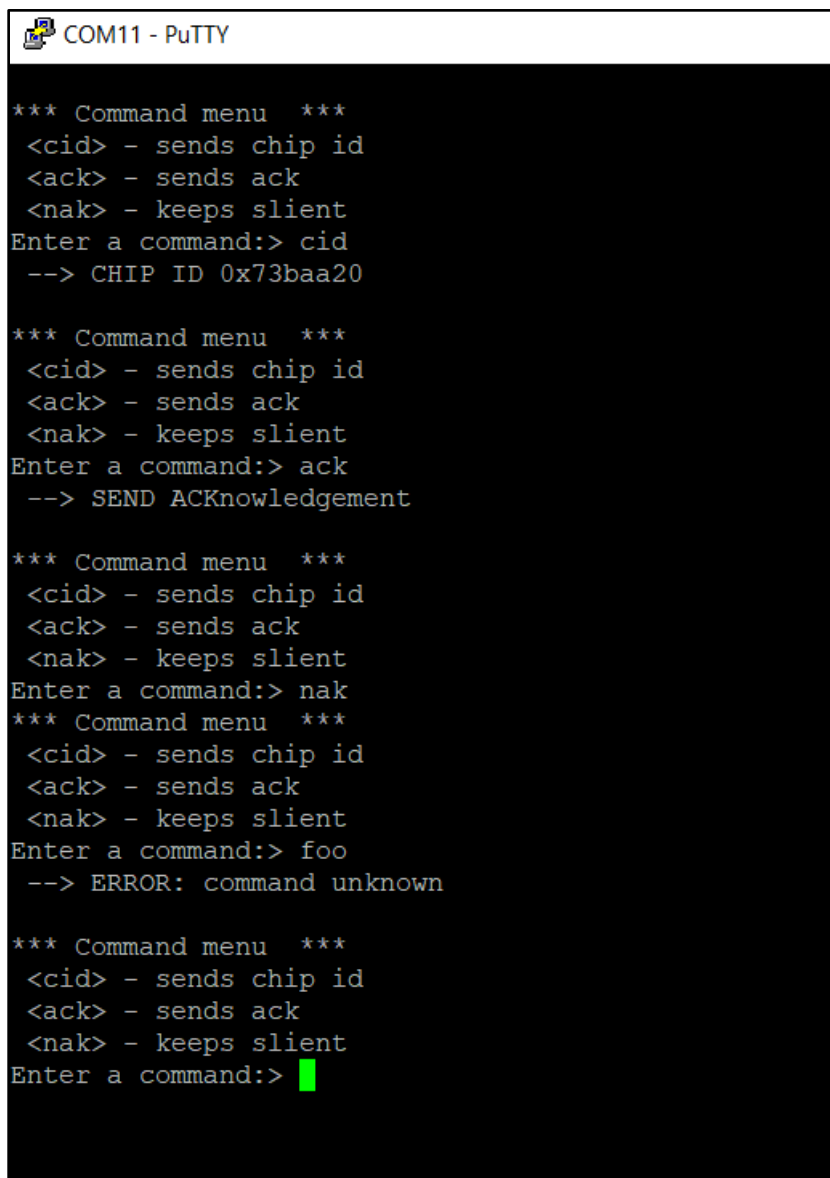


Figure 2 Transmission of ack command

The TLE9893_2QKW62S_UART_TRANSCEIVER example has implemented three commands for demonstration purpose:

- cid – returns the chip ID (demonstrates scenario request & response with individual data)
- ack – returns an acknowledgement (demonstrates scenario request & acks request without data)
- nak – keeps silent (demonstrates scenario request & no response)
- any other command will be ignored and an error response is sent out.

Figure 3 shows the execution of all those described commands and their appropriate expected response.



```
COM11 - PuTTY

*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:> cid
--> CHIP ID 0x73baa20

*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:> ack
--> SEND ACKnowledgement

*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:> nak
*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:> foo
--> ERROR: command unknown

*** Command menu ***
<cid> - sends chip id
<ack> - sends ack
<nak> - keeps slient
Enter a command:> 
```

Figure 3 All commands executed with their expected response

2 Hardware

This chapter shows how to run the TLE9893_2QKW62S_UART_TRANSCEIVER example with the TLE988X/TLE989X evaluation board. For this the project must be opened and compiled.

Figure 2 shows the TLE988X/TLE989X evaluation board. The application code must be loaded via a debugger (e.g. ULINK or J-Link) to the board. The board must be powered with 12V (red and black connections).

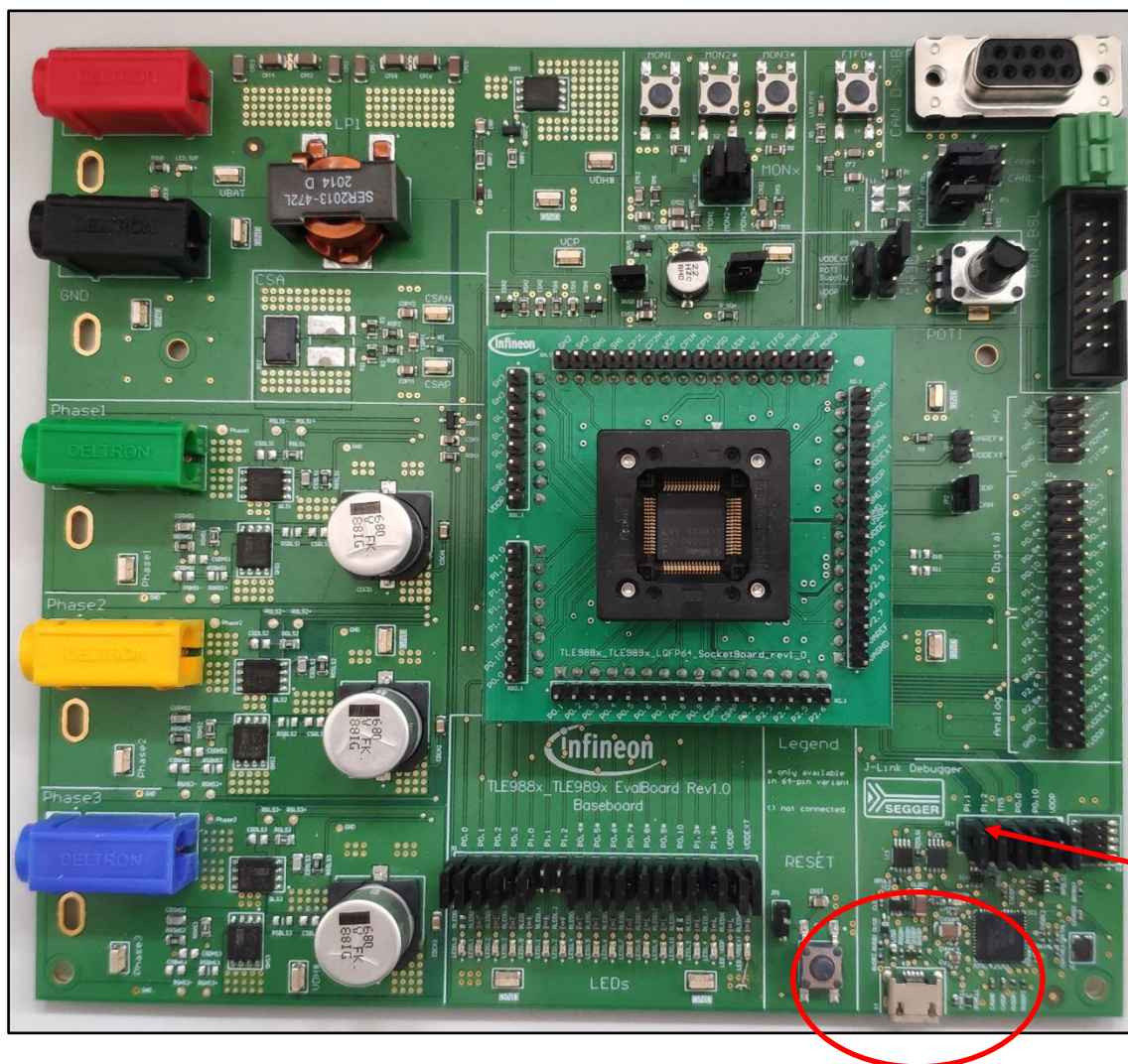


Figure 4 TLE988X/TLE989X evaluation board

The output pin P1.1 and the input pin P1.2 can be tapped on the red arrow.

Alternatively, a USB connection can be established to a local PC, which emulates a virtual COM port. The relevant COM device number can be identified via the Device Manager on Windows systems or the dmesg tool on Unix based operating systems.

In order to show the output on a command console, free tools like Putty or TeraTerm can be used. The UART1 in this example is configured with:

- a transmission baud rate of 115200,
- 8 data bits,
- 1 stop bit,
- no parity and no flow control.

3 Implementation

This chapter shows the process to follow to get a working TLE9893_2QKW62S_UART_TRANSCEIVER example.

3.1 Get the example via the Pack Installer for Keil

Open the Pack Installer within the Keil IDE. See Figure 5 below.

Choose the appropriate device (here TLE9893_2QKW62S) on the left-hand side. On the right-hand side, select the tab Examples, where you can access the TLE9893_2QKW62S_UART_TRANSCEIVER example.

Clicking on “Copy” will copy the example on your computer and open it.

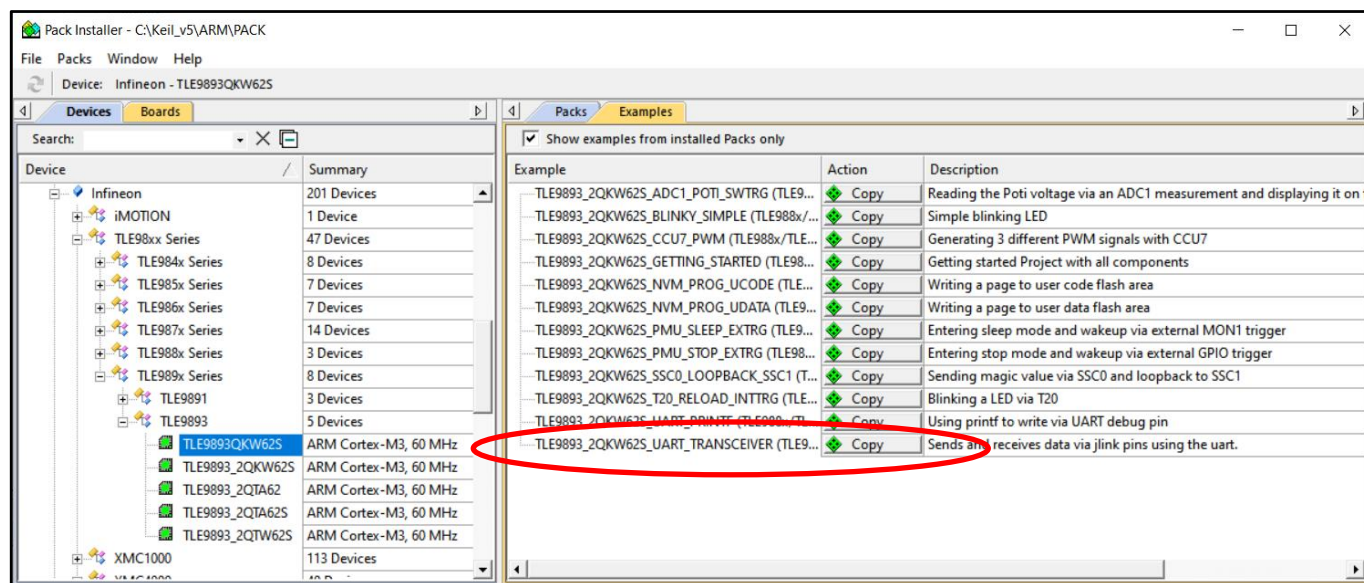


Figure 5 Keil Pack Installer

In order to redirect the stdout functions - the printf call in the example, adjust the runtime environment setting for the compiler within the Keil IDE. Select the option “User” under Compiler -> I/O -> STDOUT (see Figure 6).

In order to redirect the stdin functions - the stdin_getchar call in the example, adjust the runtime environment setting for the compiler within the Keil IDE. Select the option “User” under Compiler -> I/O -> STDIN (see Figure 6).

In the TLE9893_2QKW62S_UART_TRANSCEIVER example, the debug output and the debug input are shown. Therefore, the stdout and stdin is set. In case stderr or file access is necessary, this can be set in the Manage Run-Time Environment window as well.

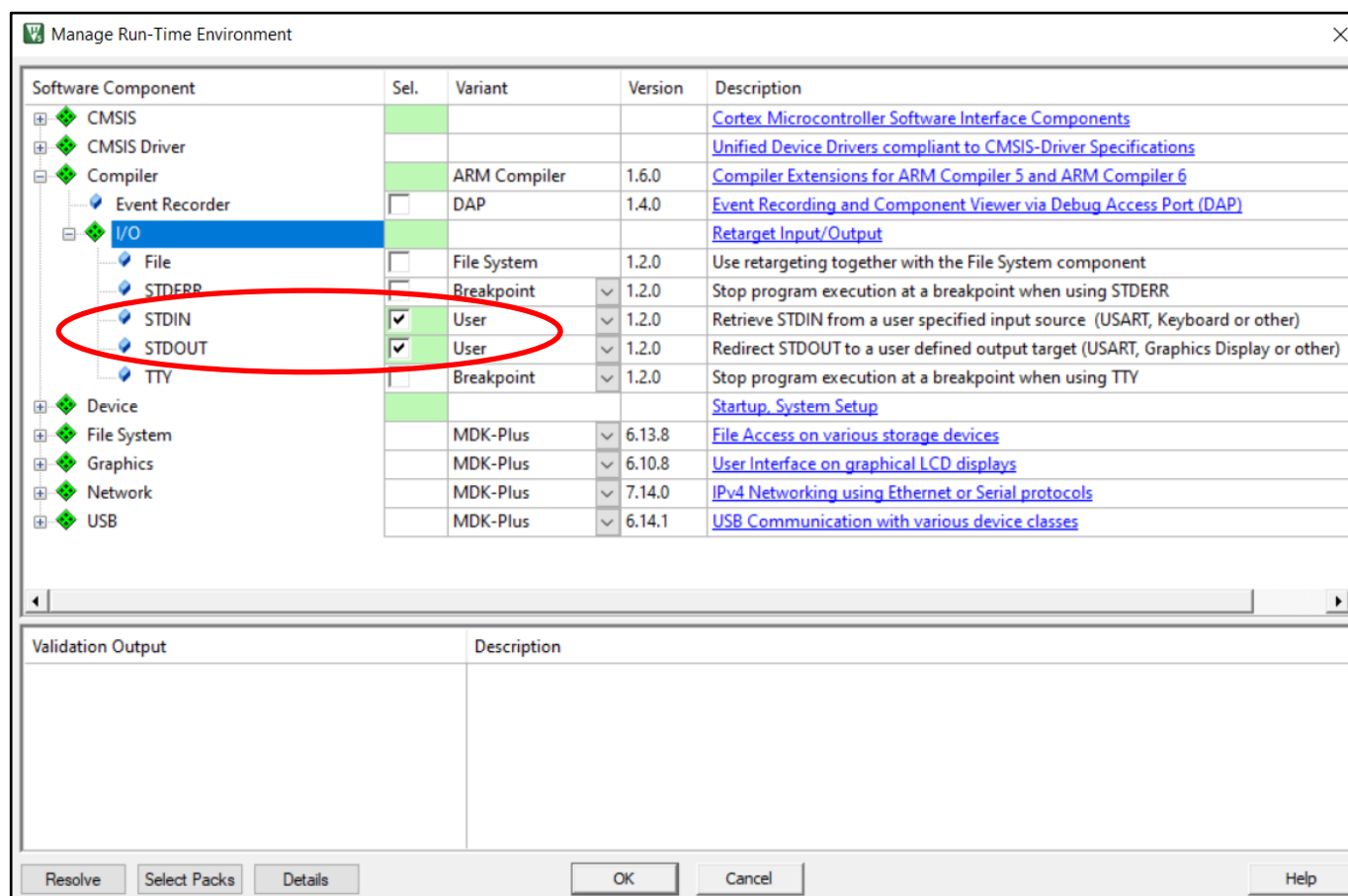


Figure 6 RTE settings for stdout and stdin

Within the runtime environment settings at the device folder tree, the correct set of the device design step must be approved. The design step is written on the device. By default, AA is selected. (see Figure 7).

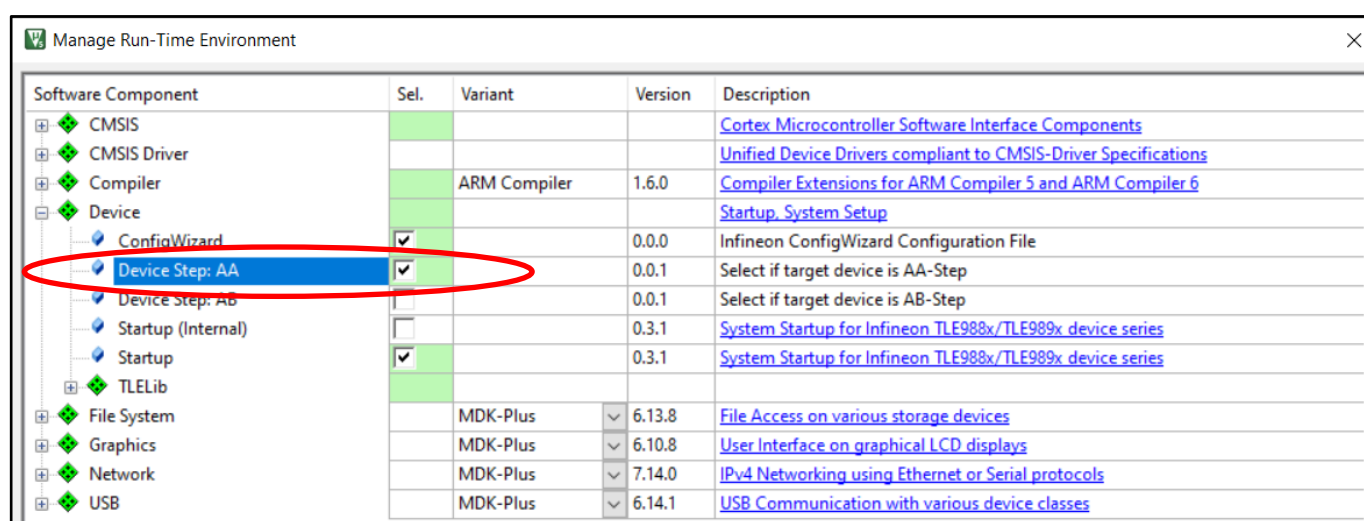


Figure 7 RTE settings for design step

See Figure 9 Config Wizard, module UART for more details.

The screenshot displays the Infineon Config Wizard interface for the UART module. The left sidebar contains the following sections, with specific settings highlighted by red circles:

- Enable UART1** (checked)
- Mode Selection**: Mode 1: 8-bit UART, variable baudrate
- Baudrate Generator Settings**:
 - Automatic Configuration: Baudrate Value = 115200 Baud
 - Manual Configuration: Baudrate Value = 19200 Baud, Effective Baudrate [Baud] = 115273, Deviation Error [%] = 0.06
- Reception Settings**:
 - Input Selection: P1.2
 - Enable Receiver of Serial Port: checked
 - Receiver Mode: RXBUF is updated if IS_RI = 1
- Transmission Settings**:
 - Transmit Start Trigger Selection: Write to TXBUF
 - Enable TXD Inversion: unchecked
 - Output Selection: P1.1
- Interrupt**:
 - Receive Interrupt: checked, Node Selection: UART_INP0_NVIC_IRQn, Enable Receive Interrupt: checked, Callback: uart_receive
 - Transmit Interrupt: unchecked
 - End Of Sync Interrupt: unchecked
 - Sync Error Interrupt: unchecked

The right side of the wizard shows the **UART1 Diagram** with the following components:

- fPER = 40 MHz** input to a **Prescaler** (divided by 1).
- Reload Value** (21) and **Frac. Divider** (n = 22) are inputs to a multiplier.
- The multiplier output is **BR = 115273 Baud**.
- Mode 1: 8-bit UART, Variable Baudrate** block containing:
 - RECEPTION**: Serial Reception Enabled, RXBUF (RB8) with MSB and LSB, RXDATA, and RI output. RXD - P1.2 is the input.
 - TRANSMISSION**: Transmit Start: Write to TXBUF, TXBUF (TB8) with MSB and LSB, TXDATA, and TXD - P1.1 output.

Figure 9 Config Wizard, module UART1

Finally, save your configuration to take these changes into account (File -> Save).

3.3 Sample code

Figure 10 shows the application code of the TLE9893_2QKW62S_UART_TRANSCEIVER example.

Within the endless loop, the `handleCommand()` method is called when the `b_cmdTrigger` value is true. The value will become true when a full command is received. This is set by the `uart_receive()` interrupt service routine (see Figure 11).

```
104   for (;;)
105   {
106       /* Main watchdog service */
107       (void) PMU_serviceFailSafeWatchdog();
108
109       /* Check for activated command handler trigger */
110       if (b_cmdTrigger == true)
111       {
112           /* Handle new command */
113           handleCommand();
114       }
115   }
116 }
```

Figure 10 Main loop in the application code

The `uart_receive()` interrupt service routine (see Figure 11) is called when a new byte is received and available in the uart receive buffer.

First, to avoid buffer overflows, the already received bytes without a newline character are checked against the maximum allowed `BUFFER_SIZE`. If the maximum is reached, the boolean `b_cmdTrigger` is set to true without receiving a newline or getting the pending character.

Otherwise the pending character is received and echoed to the stdout (to see the entered characters on the console). Next the received character is checked for a newline. If so the boolean `b_cmdTrigger` is set to true.

Finally, the receive counter is incremented before the interrupt service routine is finished.

```
165  /* UART receive ISR */
166  void uart_receive()
167  {
168      /* Check for buffer overflow */
169      if (u8_readCnt < BUFFER_SIZE)
170      {
171          /* Receive byte from P1.2 */
172          u8_buffer[u8_readCnt] = (uint8) stdin_getchar();
173
174          /* Echo byte to stdout to show character on console */
175          printf("%c", u8_buffer[u8_readCnt]);
176
177          /* Check if received character was a newline */
178          if (u8_buffer[u8_readCnt] == 0x0d)
179          {
180              /* Add a final 0x00 to the string buffer */
181              u8_buffer[u8_readCnt] = 0x00;
182
183              /* Trigger command handler */
184              b_cmdTrigger = true;
185          }
186          /* Increment receive counter for new character */
187          u8_readCnt++;
188      }
189      else
190      {
191          /* Receive buffer is full -> handle command without newline */
192          b_cmdTrigger = true;
193      }
194  }
```

Figure 11 uart_receive() interrupt service routine

Once a full command is received, the `b_cmdTrigger` value is set to `true` and the `handleCommand()` method is called (see Figure 12).

Initially no character is received and the `u8_readCnt` is 0. In that case only the command menu is written to the `stdout`.

If a command is available (`u8_readCnt` is not 0), the command will be interpreted. In this example three commands are implemented: `cid`, `ack`, `nak`:

- The 'cid' command reads out the device chip identifier and sends it back to the `stdout`.
- The 'ack' command returns an acknowledgement to the `stdout`.
- The 'nak' command just accepts the command and returns nothing.

In case the command cannot be interpreted, an error is written to the `stdout`.

Once the command is interpreted and handled, the command menu is written to the `stdout`, the `u8_readCnt` is set to 0 and the `b_cmdTrigger` is set to `false`. The system is now waiting for a new command again.

```

118  /* Command handler */
119  void handleCommand()
120  {
121      /* Declare and initiate chip ID value */
122      uint32 u32_chipId = 0;
123
124      /* Check for initial call - only the menu must be printed */
125      if (u8_readCnt != 0)
126      {
127          /* Check for 'cid' command */
128          if (strcmp((void *) u8_buffer, "cid") == 0)
129          {
130              /* Read unique chip ID */
131              user_cid_get(&u32_chipId);
132              /* Send out chip ID via stdout */
133              printf("\n --> CHIP ID 0x%x\n", u32_chipId);
134          }
135          /* Otherwise check for 'ack' command */
136          else if (strcmp((void *) u8_buffer, "ack") == 0)
137          {
138              /* Send out acknowledgement via stdout */
139              printf("\n --> SEND ACKnowledgement\n");
140          }
141          /* Otherwise check for 'nak' command */
142          else if (strcmp((void *) u8_buffer, "nak") == 0)
143          {
144          }
145          /* Otherwise unknown command */
146          else
147          {
148              /* Send error message via stdout */
149              printf("\n --> ERROR: command unknown\n");
150          }
151      }
152
153      /* Print a fancy simple command menu */
154      printf("\n*** Command menu ***\n");
155      printf(" <cid> - sends chip id\n");
156      printf(" <ack> - sends ack\n");
157      printf(" <nak> - keeps silent\n");
158      printf("Enter a command:> ");
159      /* Reset u8_readCnt for new command */
160      u8_readCnt = 0;
161      /* Deactivate command handler until new one is received */
162      b_cmdTrigger = false;
163  }

```

Figure 12 Command handler

References

See the code examples at www.infineon.com

Revision history

Document version	Date of release	Description of changes
1.0	2021-10-22	Initial version
1.1	2022-10-13	Editorial changes

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