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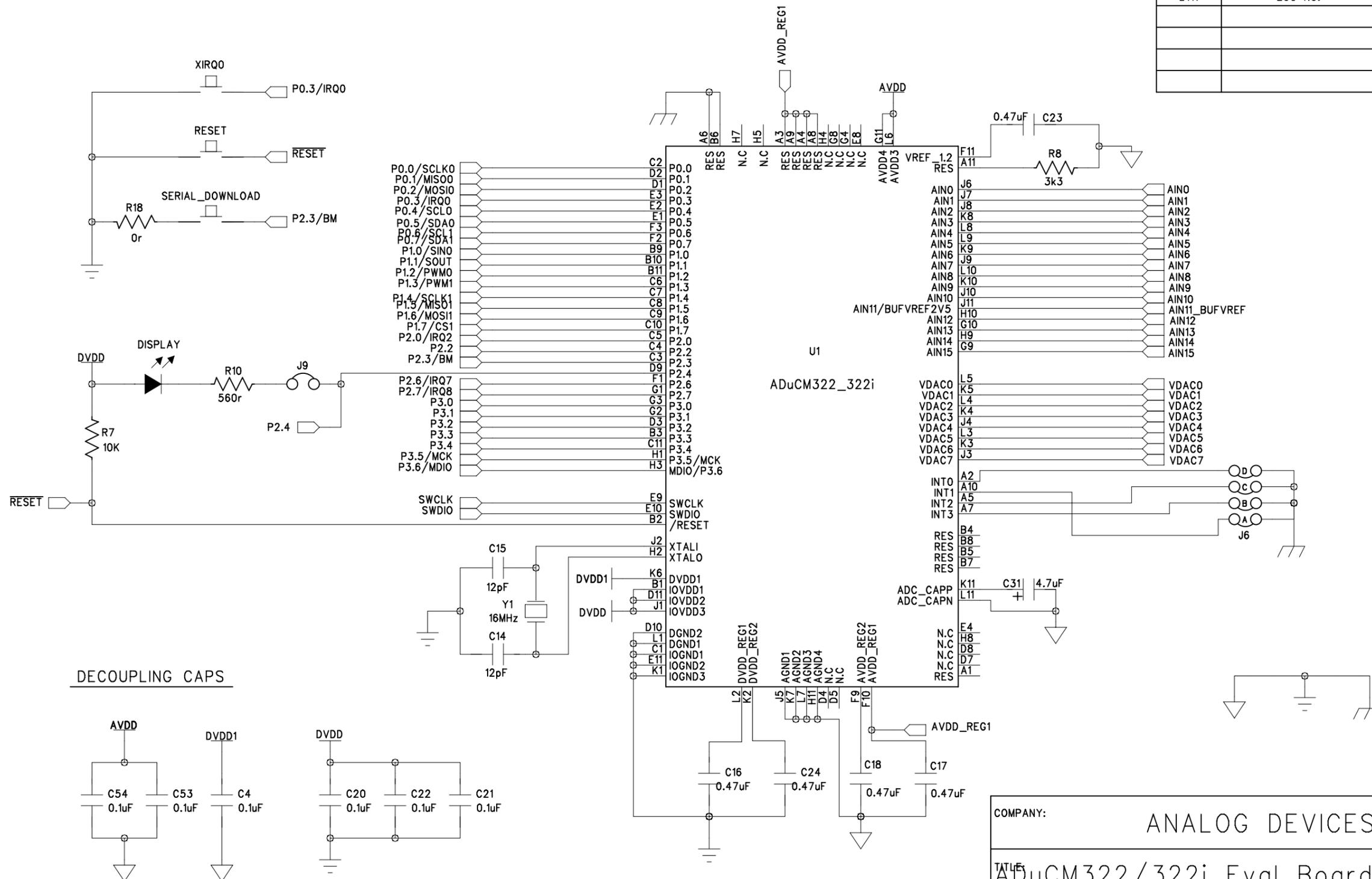
4

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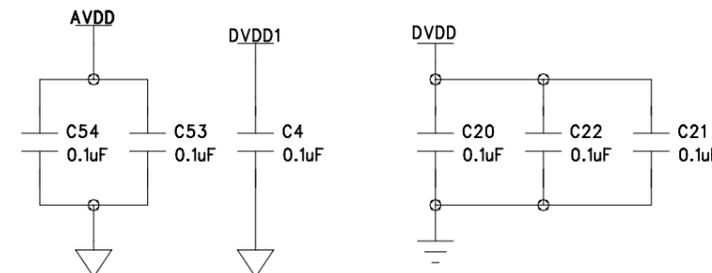
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DECOUPLING CAPS



COMPANY: ANALOG DEVICES

TITLE: ADuCM322/322i Eval Board Schematic  
Main Device/Socket

DRAWN: Padraic O'Reilly	DATED: 24/09/2015
CHECKED: TBD	DATED: XX/XX/2015
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
EVAL-ADuCM322_322iEBZ			BZ

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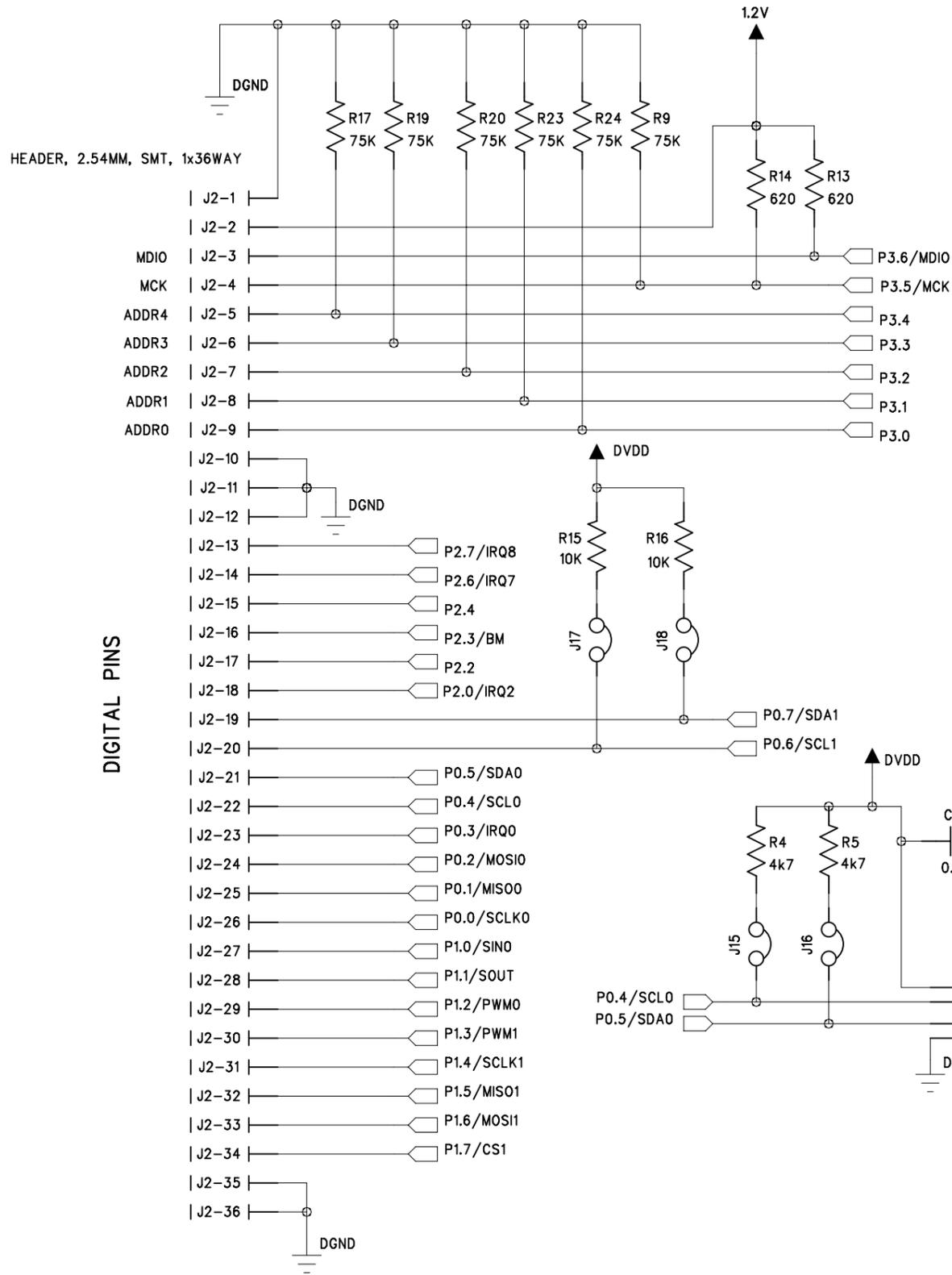
4

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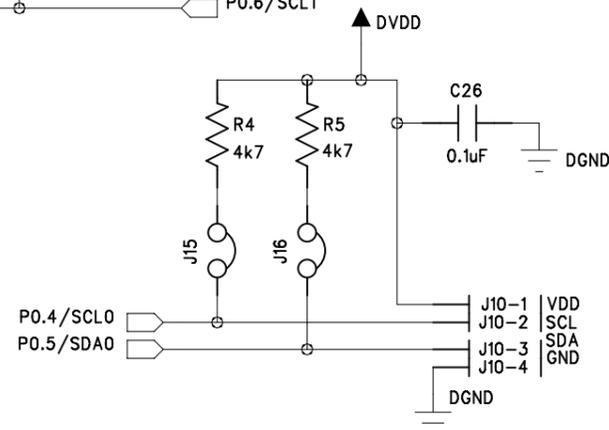
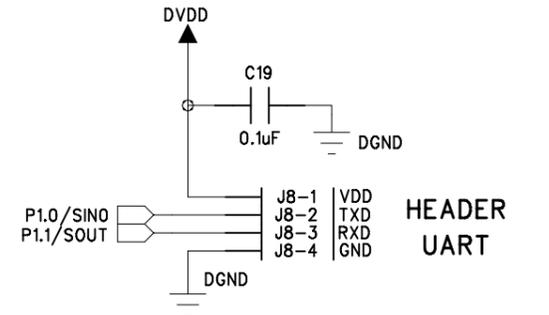
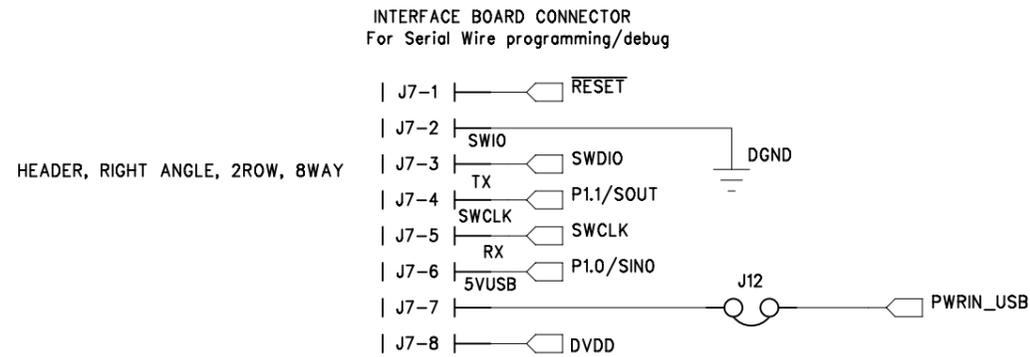
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DIGITAL PINS



COMPANY: ANALOG DEVICES			
TITLE: ADuCM322/322i Eval Board Schematic Digital I/O			
DRAWN: Padraic O'Reilly	DATED: 24/09/2015	CODE:	SIZE:
CHECKED: TBD	DATED: XX/XX/2015	DRAWING NO:	REV:
QUALITY CONTROL:	DATED:	EVAL-ADuCM322_322iE	
RELEASED:	DATED:	SCALE:	SHEET: 2 OF 7

DRAWN: Padraic O'Reilly	DATED: 24/09/2015
CHECKED: TBD	DATED: XX/XX/2015
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

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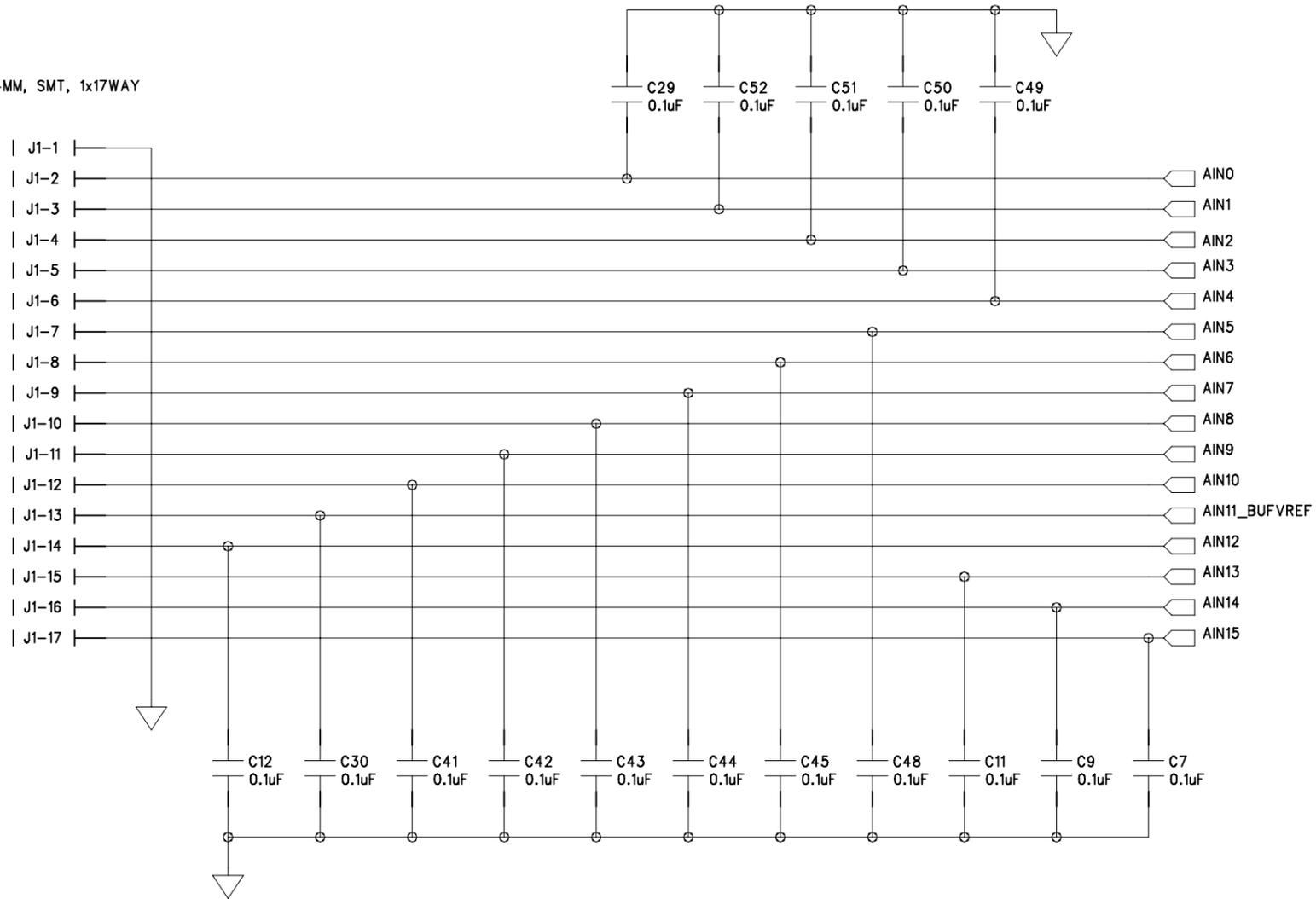
2

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

HEADER, 2.54MM, SMT, 1x17WAY

ANALOG I/O



COMPANY:				ANALOG DEVICES			
TITLE:				ADuCM322/322i Eval Board Schematic			
				ADC - Connector			
DRAWN: Padraic O'Reilly	DATED: 24/09/2015	CODE:	SIZE:	DRAWING NO:	REV:		
CHECKED: TBD	DATED: XX/XX/2015			EVAL-ADuCM322_322iEBZ			
QUALITY CONTROL:	DATED:						
RELEASED:	DATED:						
SCALE:				SHEET: 3 OF 7			

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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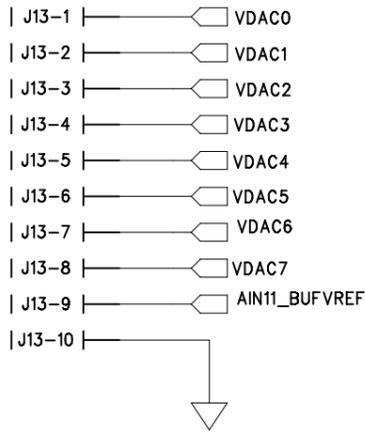
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HEADER, 2.54MM, SMT, 1x10WAY

VDAC CONNECTOR



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COMPANY: ANALOG DEVICES

TITLE: ADuCM322/322i Eval Board Schematic  
IDAC/VDAC

DRAWN: Padraic O'Reilly	DATED: 24/09/2015
CHECKED: TBD	DATED: XX/XX/2015
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADuCM322_322iE	BY

SCALE: SHEET: 4 OF 7

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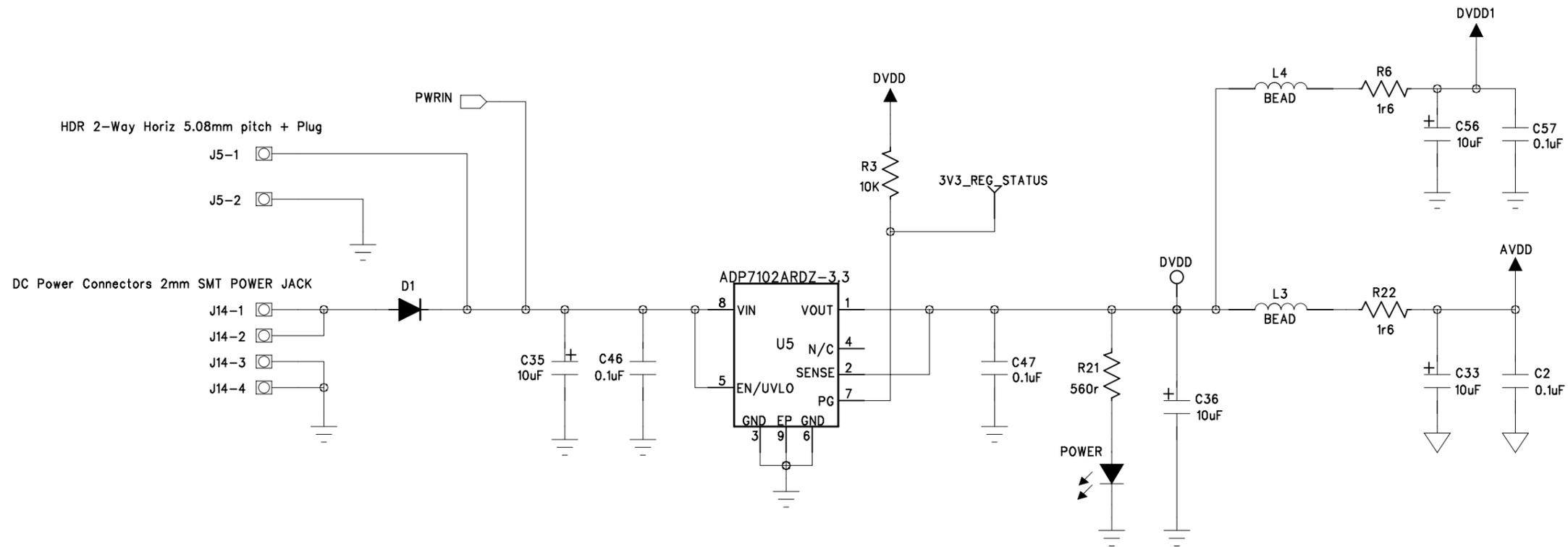
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES

TITLE: ADuCM322/322i Eval Board Schematic  
POWER-1

DRAWN: Padraic O'Reilly	DATED: 24/09/2015
CHECKED: TBD	DATED: XX/XX/2015
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADuCM322_322iEBZ	

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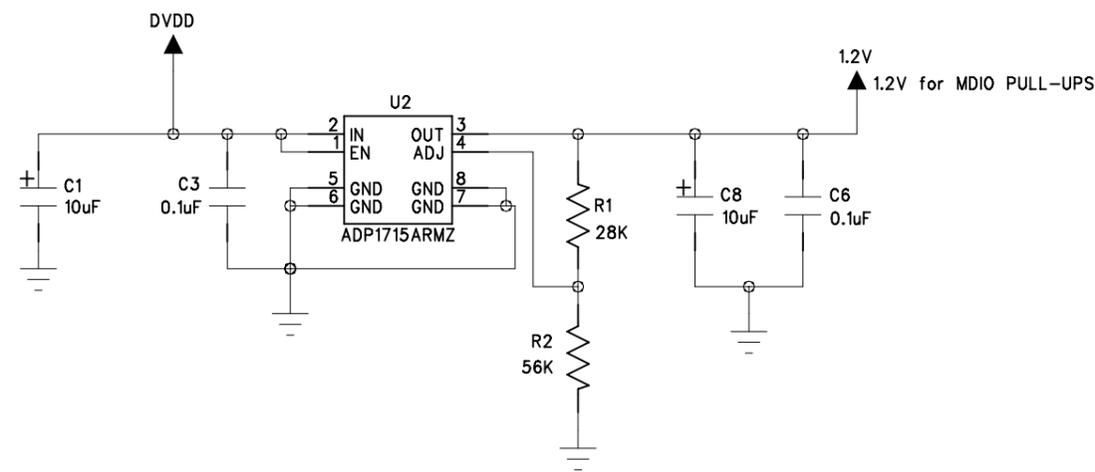
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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COMPANY:				ANALOG DEVICES			
TITLE							
ADuCM322/322i Eval Board Schematic							
POWER-2							
DRAWN:		DATED:		CODE:		DRAWING NO:	
Padraic O'Reilly		24/09/2015					
CHECKED:		DATED:		SIZE:		REV:	
TBD		XX/XX/2015					
QUALITY CONTROL:		DATED:		EVAL-ADuCM322_322iEBZ			
RELEASED:		DATED:		SCALE:			
				SHEET: 6 OF 7			

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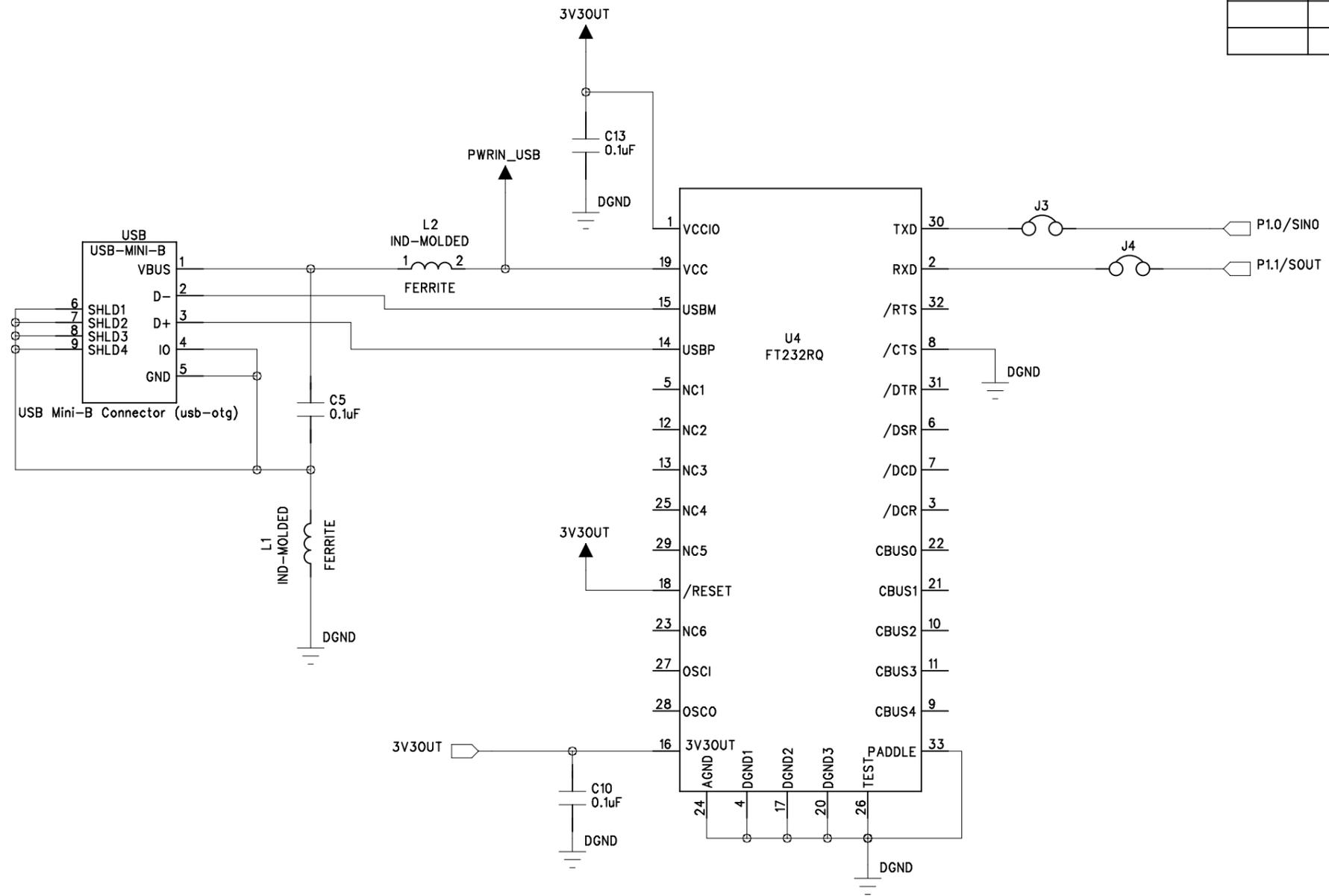
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES

TITLE: ADuCM322/322i Eval Board Schematic  
UART-USB

DRAWN: Padraic O'Reilly	DATED: 24/09/2015
CHECKED: TBD	DATED: XX/XX/2015
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE:	DRAWING NO:	REV:
EVAL-ADuCM322_322iE		BZ	
SCALE:			SHEET: 7 OF 7

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