



## Install Keil MDK Version 5

Download and install the MDK Core from <u>www.keil.com/mdk5/install</u>

Add Software Pack using the Pack Installer

• Infineon::XMC4000\_DFP – Infineon XMC4000 Series Device Support

### **Power up the board**

- Connect the board to the PC using the Debug Micro USB Connector
- Windows will recognize the USB device and install the required drivers automatically

 Verify Installation using example projects provided in the Software Pack or download examples that apply to this board

 www.keil.com/dd2/infineon/xmc4500\_f100x1024

• Copy an example using the Pack Installer. Compile, download to Flash, and debug the application. The provided examples are ready to run.

## More Information

• Board Information

www.keil.com/boards2/infineon/xmc4500relaxlitekit

## **Related Products**

- MDK Version 5
- Getting Started User's Guide
- MDK-Professional Middleware
- Learning Platform

www.keil.com/mdk5 www.keil.com/gsg www.keil.com/middleware www.keil.com/learn

# **Debug and Trace Configuration**

Infineon XMC4500 Relax Lite Board



# J-LINK / J-TRACE Cortex

#### 👿 Options for Target 'Blinky' Select the J-LINK / J-TRACE ж Cortex driver on the Debug tab. Device Target Output Listing User C/C++ Asm Linker Debug Utilities C Use Simulator with restrictions Settings Use: J-LINK / J-TRACE Cortex ✓ Settings Limit Speed to Real-Time ✓ Load Application at Startup ✓ Run to main() Load Application at Startup Run to main() Cortex JLink/JTrace Target Driver Setup × Click on **Settings** and select the **SW** Debug Trace Flash Download Port to configure the Serial Wire -J-Link / J-Trace Adapter SW Device Debug (SWD) port. Move 551010533 SN: -IDCODE Device Name SWD Ox2BA01477 ARM CoreSight SW-DP Device: J-Link Lite-XMC4000 Rev.1 HW : V1.00 dll : V5.10 FW : J-Link Lite-XMC4000 Rev.1 ci C Automatic Detection Port: Max Clock: C Manual Configuration SW • 5 MHz • Auto Clk Add Delete Update IR len:

## Trace Settings

For trace capability, configure ITM Trace by selecting the **Trace** tab. Check **Enable** in **Trace Settings** and set the **Core Clock** speed to correspond to the CPU Core Clock.

Set **Trace Port** to Serial Wire Output – UART/NRZ.

Ensure to have a maximum speed of *1.000000 MHz* for the SWO Clock.

Cortex JLink/JTrace Target Driver Setup		
Debug Trace Rash Download		
I▼ Enable	Core Clock: 10.000000 MHz	Trace Cache Lines 2M 💌 Use Cache File (max. 1GB)
Trace Port Serial Wire Output - UART/N ✓ SWO Settings Prescaler: Core Clk / 10 Clock: 1.000000 MHz ✓ Autodetect max SWO Clk	PC Sampling Prescaler: 1024*16 Periodic Period: <disabled> on Data R/W Sample ETM Trace Enable</disabled>	Timestamps         ✓ Enable       Prescaler:         Trace Events         CPI: Cycles per Instruction         EXC: Exception overhead         SLEEP: Sleep Cycles         LSU: Load Store Unit Cycles         FOLD: Folded Instructions